



Docket No. 740819-1043

Serial No. 10/766,892

Page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Hiroaki FUJIMOTO et al.) Group Art Unit: 2815
Serial No. 10/766,892) Examiner: Sheila L. Clark
Filed: January 30, 2004) Confirmation No. 4196
For: STACKED SEMICONDUCTOR CHIPS) Date: September 7, 2005
ATTACHED TO A WIRING BOARD)

CERTIFICATE OF MAILING OR TRANSMISSION
[37 CFR 1.8(a)]

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Mail Stop _____, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, or being facsimile transmitted to the USPTO at _____, on September 7, 2005.

Signature: _____

Name: _____

AMENDMENT AFTER FINAL REJECTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 7, 2005, please amend the above-identified application as follows.